

Express Mail No.  
EK708049091

Applicant: Martin Langhammer  
Att'y. Docket: 174/205  
Title: DSP PROCESSOR ARCHITECTURE  
WITH WRITE DATAPATH WORD  
CONDITIONING AND ANALYSIS

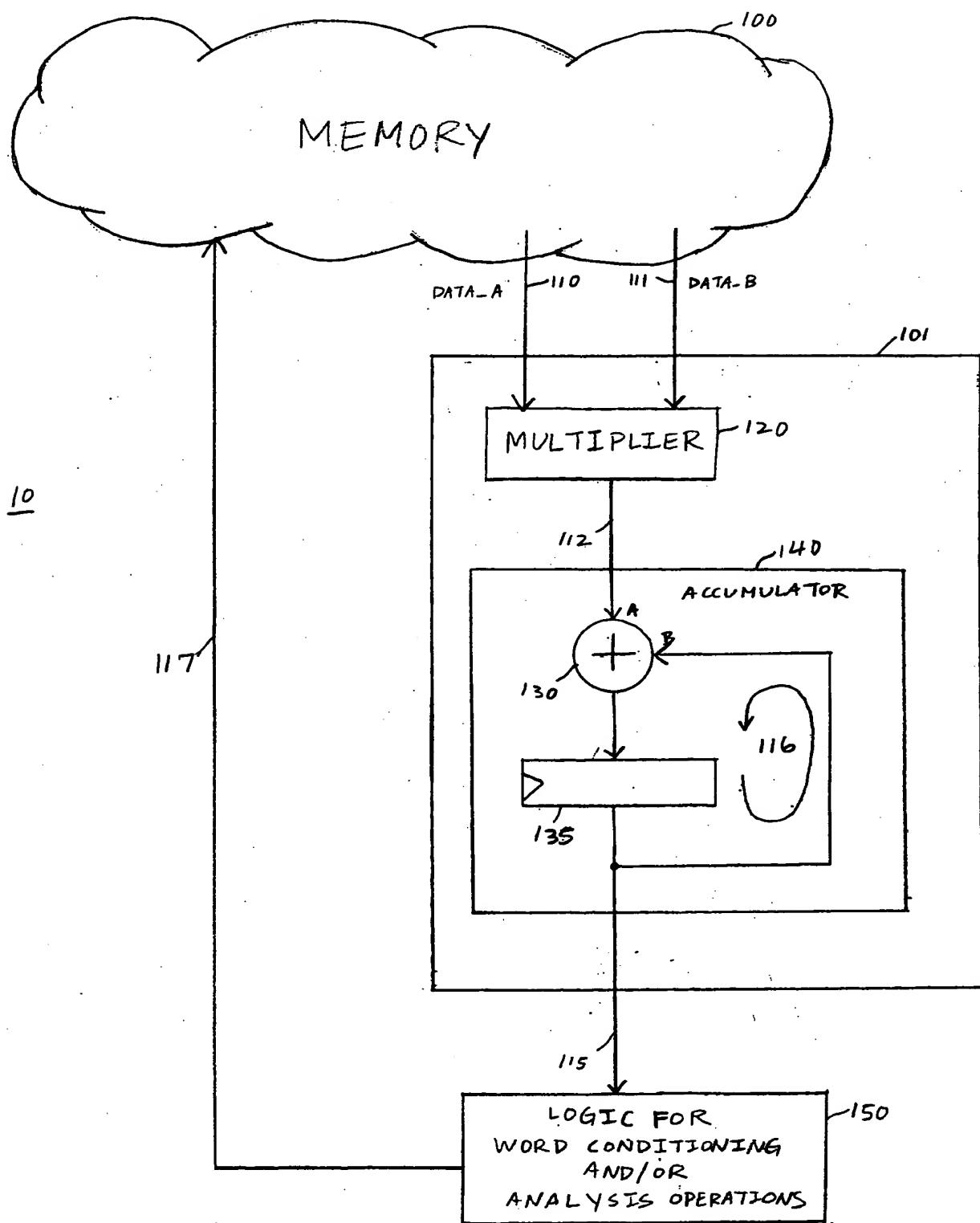


FIG. 1a

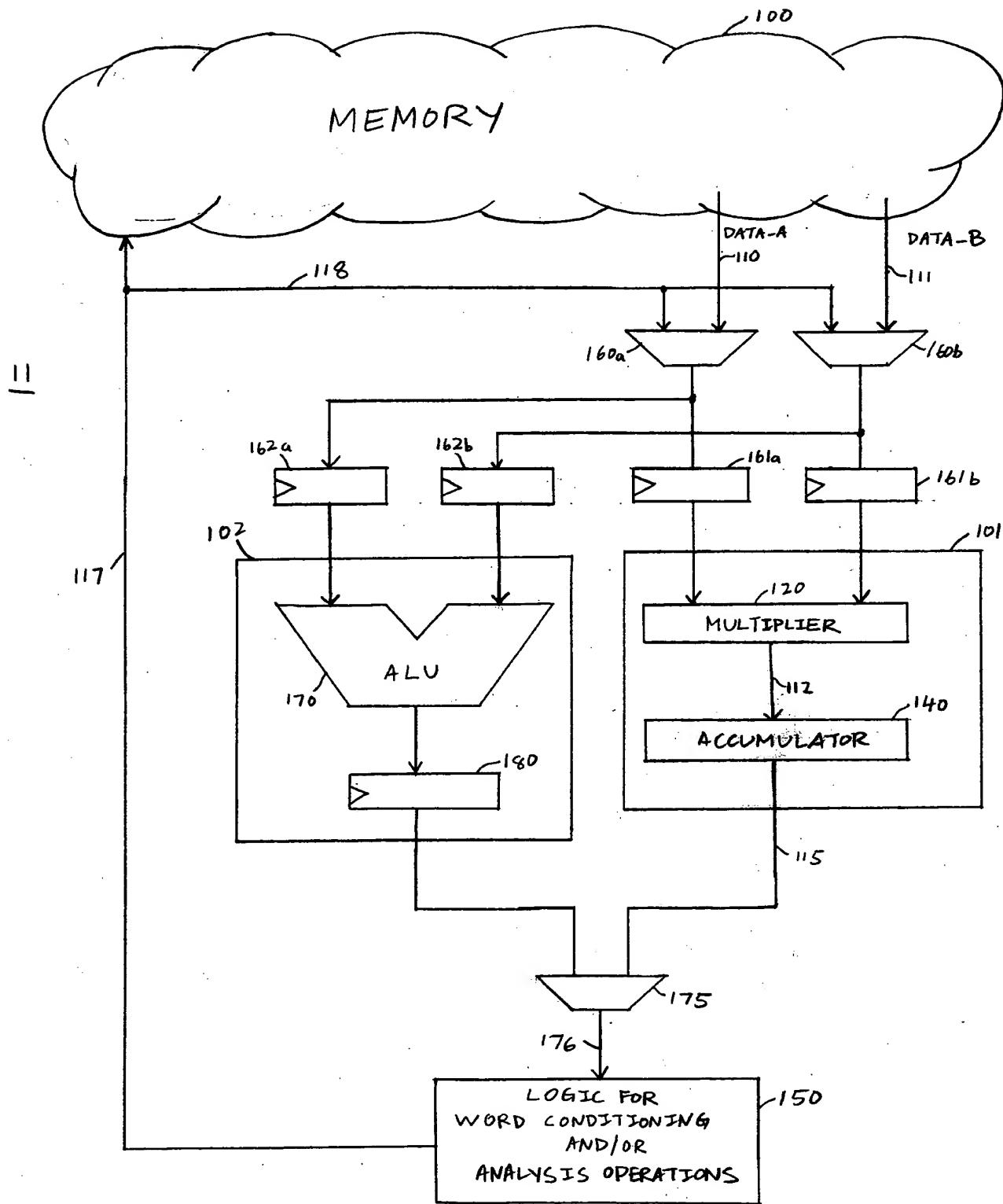


FIG. 1b

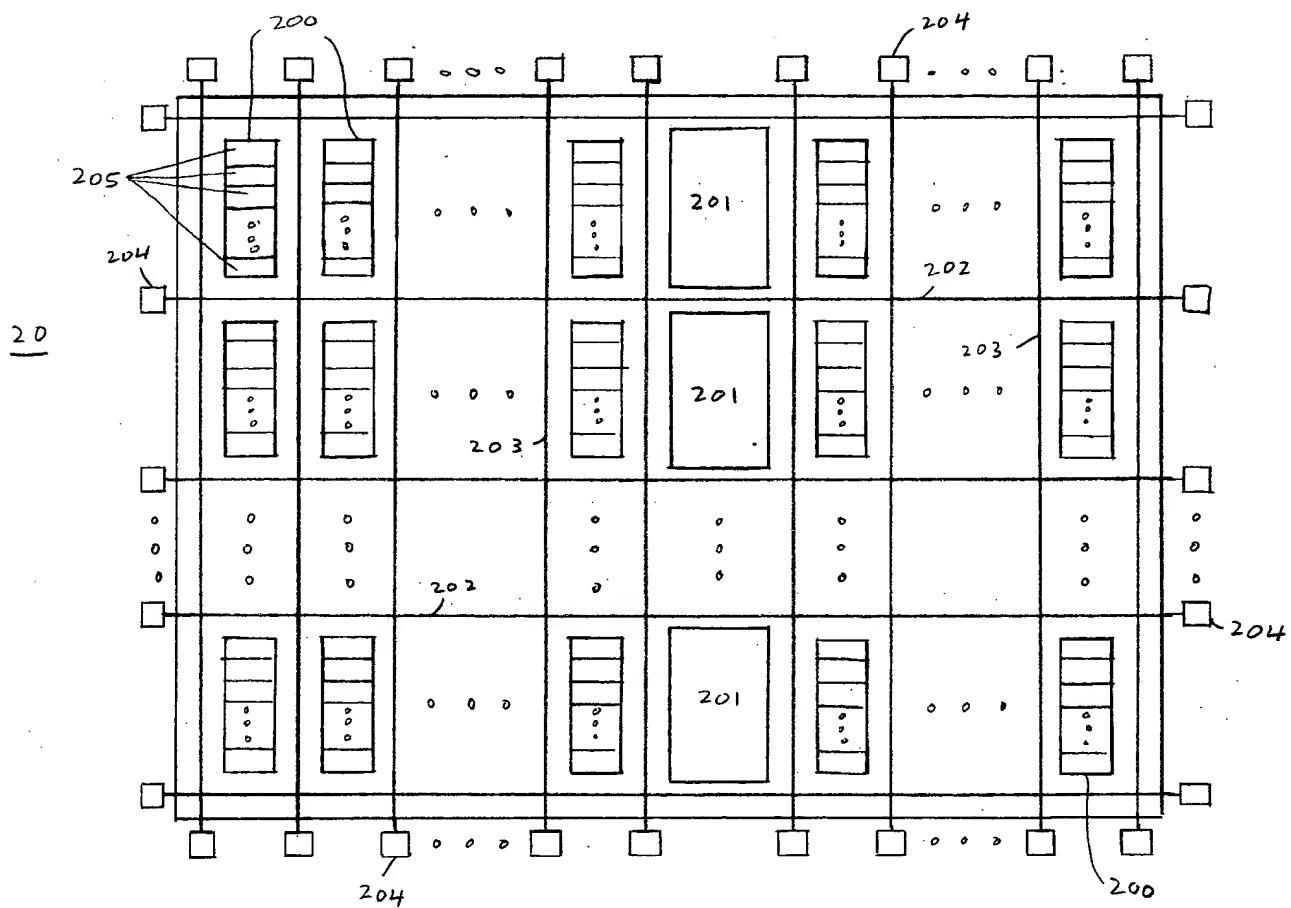


FIG. 2

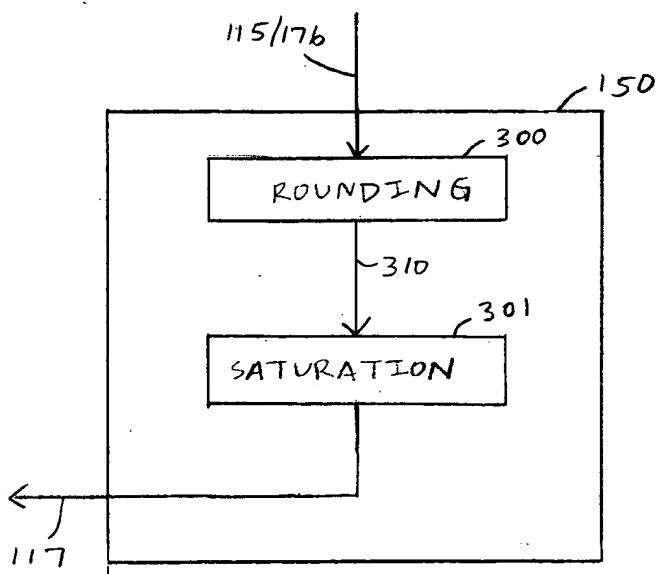


FIG. 3

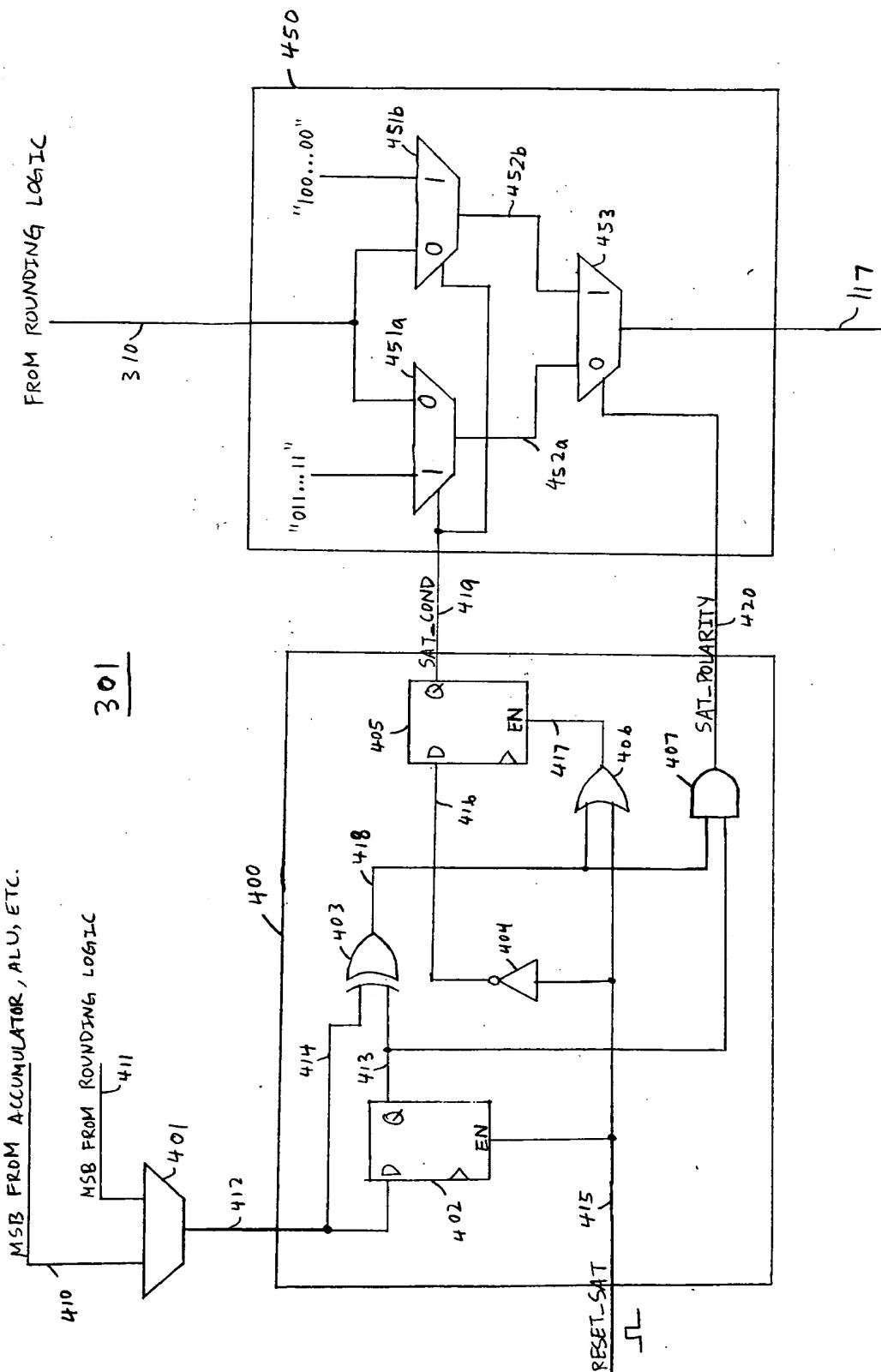


FIG. 4

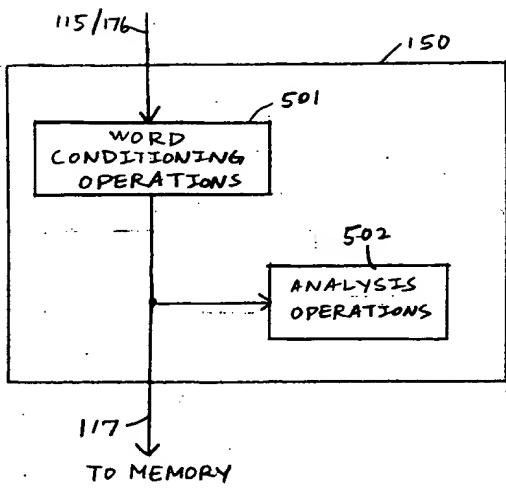


FIG. 5a

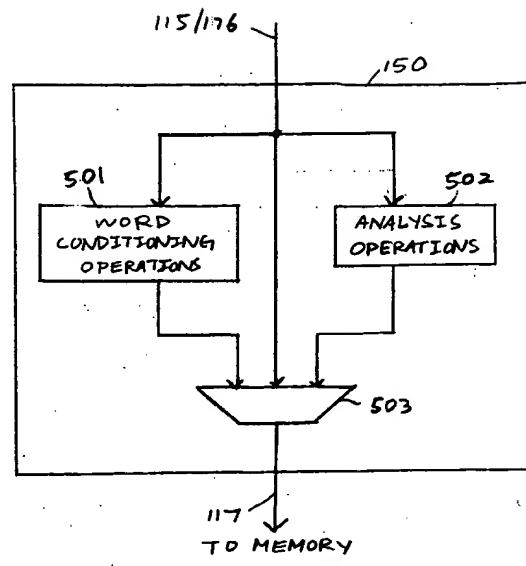


FIG. 5b

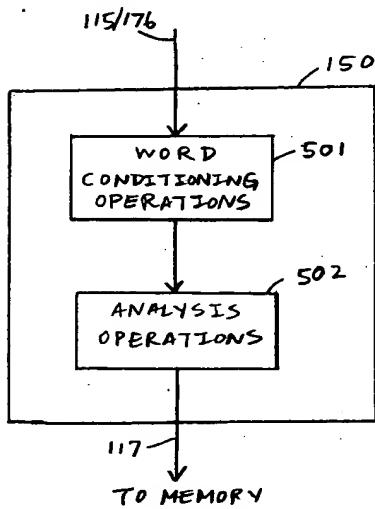


FIG. 5c

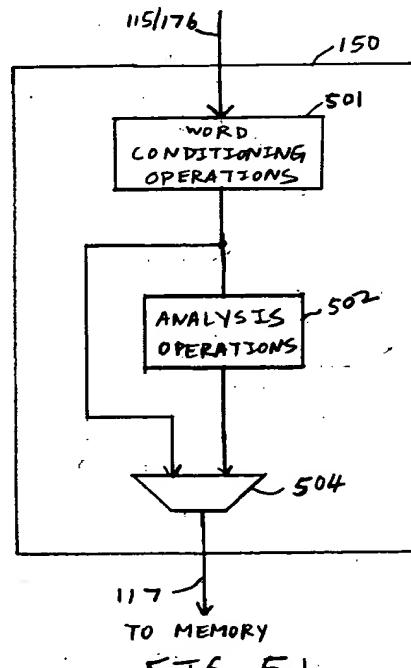


FIG. 5d

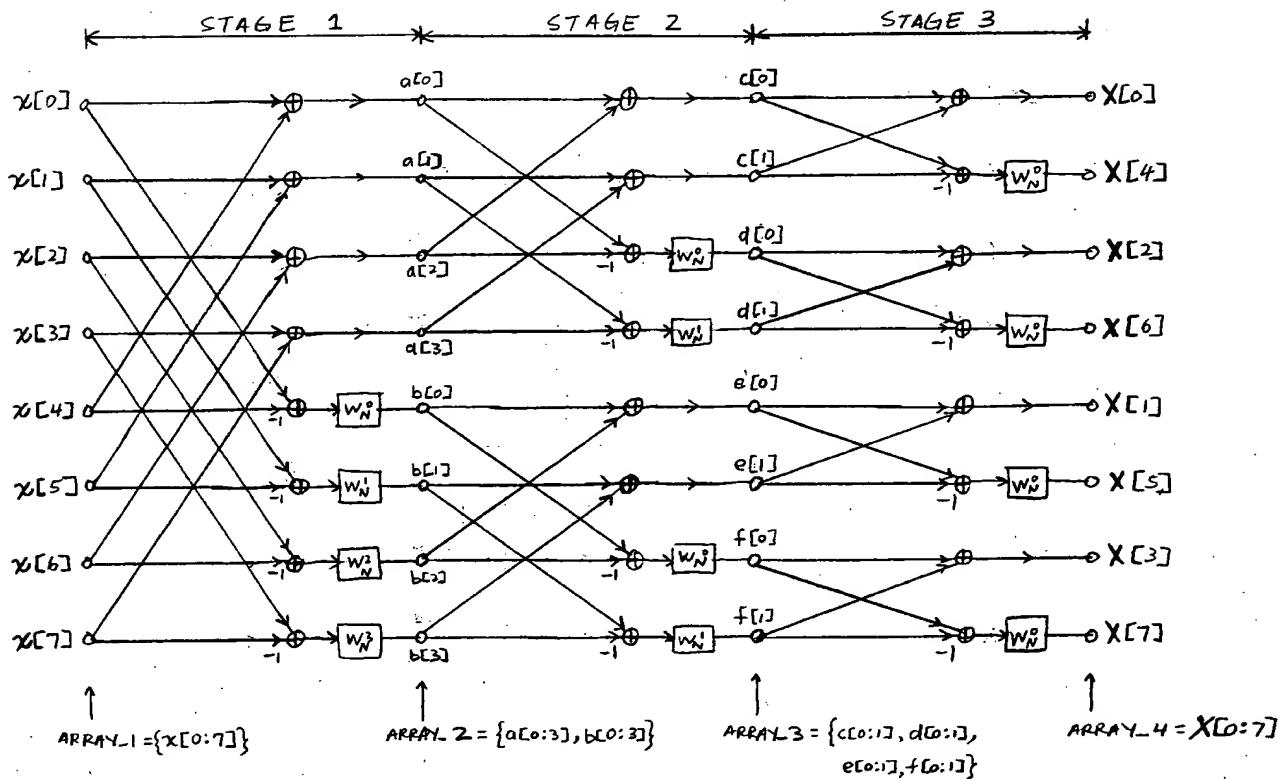


FIG. 6

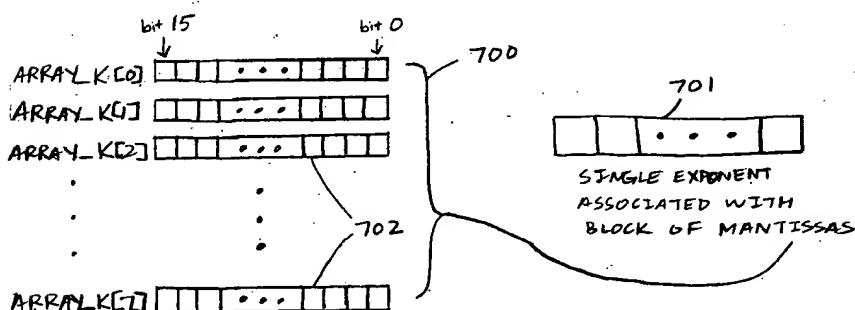


FIG. 7

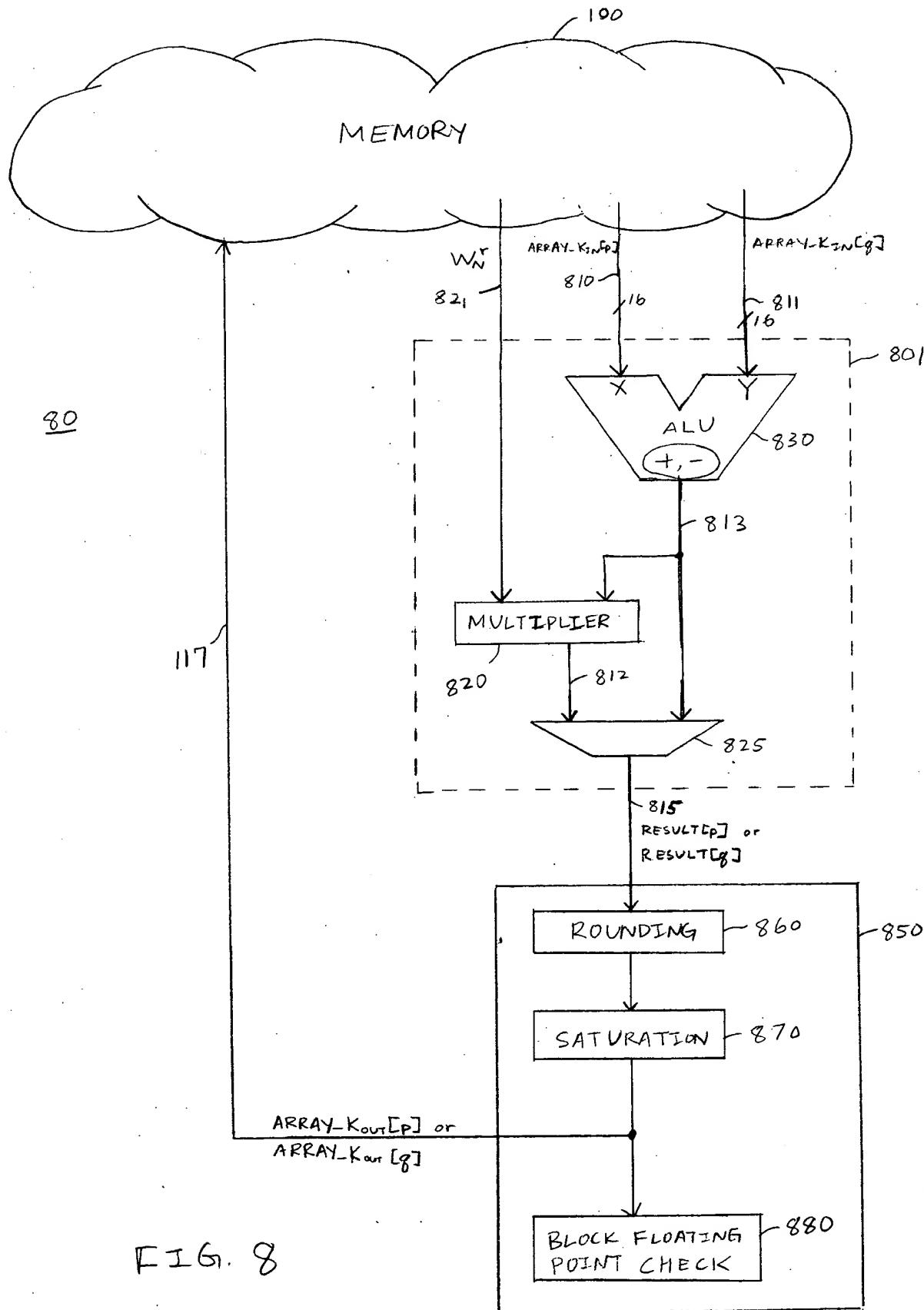


FIG. 8

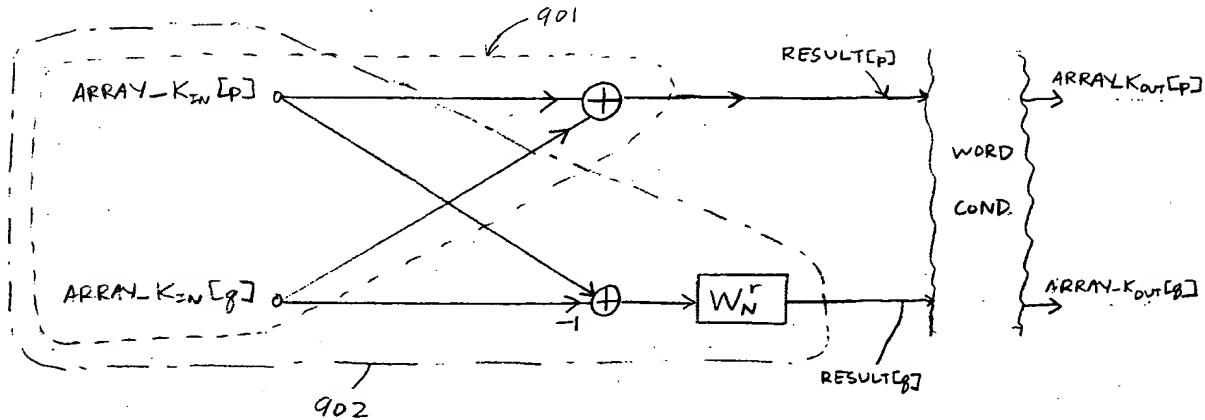


FIG. 9

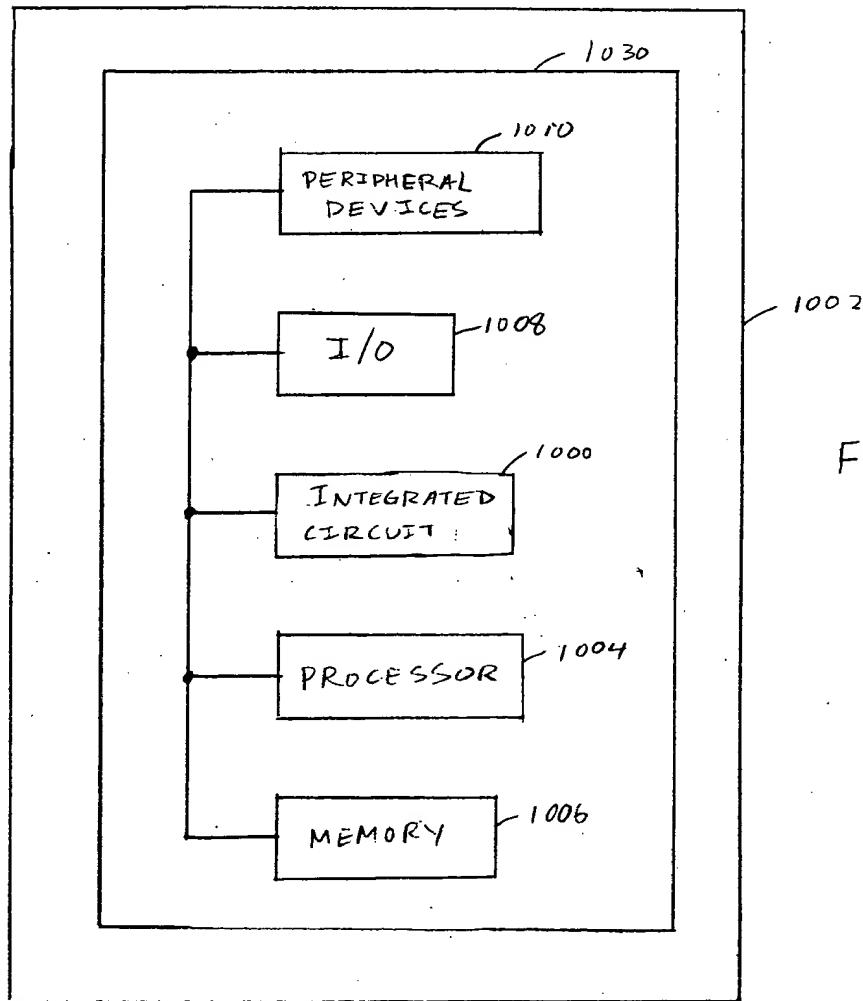


FIG. 10